processor 16f877

include <p16f877.inc>

valor1 equ h'21'

valor2 equ h'22'

valor3 equ h'23'

cte1 equ 20h

cte2 equ 50h

cte3 equ 60h

ORG 0

GOTO INICIO

ORG 5

INICIO: BSF STATUS,RPO ;Banco 1

BCF STATUS,RP1

MOVLW H'0' ;PORTB Salida

MOVWF TRISB ; Configurando al puerto B como salida

BCF STATUS,RPO ;Banco 0

CLRF PORTB ;PORTB = 0000000

loop2 BCF PORTB,0 ;B0=1

CALL retardo ;LLAMADO A SUBRUTINA

BCF PORTB,0 ;B0 = 0

CALL retardo

GOTO loop2

retardo MOVLW cte1

MOVWF valor1 ;valor1 = 20

tres MOVLW cte2

MOVWF valor2 ;valor2 = 50

dos MOVLW cte3

MOVWF valor3 ;valor3 = 60

uno DECFSZ valor3

GOTO uno

DECFSZ valor2

GOTO dos

DECFSZ valor1

GOTO tres

RETURN

END